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Application Serial No.: 10/658700
For the Office Action Dated: December 29, 2006

MAR 29 2007

IN THE SPECIFICATION

Amendments to the Title:

Please amend the title to:

~~IMAGING DEVICE CHIP SET AND IMAGE PICKUP SYSTEM~~
IMAGING DEVICE CHIP HAVING TRANSISTORS OF SAME
CONDUCTIVITY TYPE AND IMAGE PICKUP SYSTEM

Amendments to the Specification:

Please amend the following at page 2, lines 21-34 of the specification:

FIG. 8 is a block diagram illustrating a configuration of another conventional imaging device chip set 90. The imaging device chip set 90 includes an imaging chip 210 and a DSP chip 211. The imaging device chip set 90 in which the imaging chip 210 and the DSP chip 211 are arranged independently is configured so that, irrespective of the signal processing method and the type of the DSP, a vertical scanning circuit 205 and a horizontal scanning circuit 206 for driving a sensor part 207, a timing pulse generating circuit 203 for generating pulses necessary for the vertical scanning circuit 205 206 and the horizontal scanning circuit 206 208, a gain control amplifier 204 for amplifying a signal output from the sensor part 207, and an analog-digital converting circuit 208 for converting an output signal from the gain control amplifier 204 to a digital signal are mounted on an imaging chip 210, so as to cause the imaging chip to operate independently. A digital signal processing circuit 98 is mounted on a DSP chip 211.

Please amend the following at page 3, line 26-page 4, line 3 of the specification:

In the case where the vertical scanning circuit 205 206 and the horizontal scanning circuit 206 208 are formed with CMOS logics, the current fluctuations are caused by a so-called flow-through current that is generated upon the switching of the CMOS circuits. It is also known that generally, a CMOS circuit is characterized by small power consumption, but a great current (flow-through current) flows at the moment of switching of the CMOS circuit. This is because only at an instant upon the switching,

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both of n-MOS and p-MOS transistors are turned ON, which causes a short circuit between a power source and a ground. In the case where a line for controlling the switching is arranged outside the chip, noises are superposed on the line itself, or pulses passing through the line are dulled, thereby increasing noises of the power source due to the flow-through current. Therefore, the earlier possible establishment of the two-chip configuration system technology with which requirements of the lower cost and the higher performance are satisfied is being demanded.